HIGH VOLTAGE ENGINEERING ELECTRICAL ENGINEERING DEPARTMENT

STUDENT RECORD

NAME : PATURU PRADEEP

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MINI PROJECT: A MATRIX-LIKE TOPOLOGY FOR HIGH – VOLTAGE

GENERATION

SEMINAR : A NOVEL MODEL OF HVDC SUPERCONDUCTING CIRCUIT

BREAKER AND ITS PERFORMANCE

MAIN PROJECT: DEVELOPMENT OF NATURAL ESTER BASED NANO FLUIDS

FOR TRANSFORMER INSULATION

PUBLICATIONS/

ACHIEVEMENTS: None