

HIGH VOLTAGE ENGINEERING ELECTRICAL ENGINEERING DEPARTMENT

STUDENT RECORD



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MINI PROJECT : *HYBRID HVDC CIRCUIT WITH SELF POWERED GATED DRIVES*

SEMINAR : *DEVELOPMENTS IN HVDC CIRCUIT BREAKERS WITH POWER ELECTRONIC DEVICES*

MAIN PROJECT: *DETECTION AND CLASSIFICATION OF CABLE DEFECTS BY PARTIAL DISCHARGE MEASUREMENTS*

PUBLICATIONS/ACHIEVEMENTS: *None*