

### Declaration

I hereby declare that the information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration. I also undertake the responsibility to inform the co-ordinators sufficiently in advance, in case I am unable to attend the course.

Place:

Date: Signature of the applicant

### SPONSORSHIP CERTIFICATE

Certified that Mr./ Ms./ Dr. ....

.....  
is an employee of this institution and is hereby sponsored for the FDP on “Recent trends and applications of High Voltage Engineering” at **National Institute of Technology Calicut** during the period of 22<sup>nd</sup> June – 28<sup>th</sup> June, 2014. He/she will be permitted to attend the course, if selected.

Place: Name & Signature

Date: of the Sponsoring Authority  
(*seal of the institution*)

### **Address for Correspondence:**

Dr. Sindhu. T.K, Asst. Professor

OR

Dr. Preetha P., Associate Professor,  
Coordinator,

**RTAHVE,**

Electrical Engineering Department,

National Institute of Technology Calicut,

NIT Campus P. O., Calicut- 673601, Kerala, India

**e-mail:** [tk\\_sindhu@nitc.ac.in](mailto:tk_sindhu@nitc.ac.in) / [preetha@nitc.ac.in](mailto:preetha@nitc.ac.in)

**Phone:** 0495-2286363/2286315, Fax: 0495-2287250

**URL:** [www.nitc.ac.in](http://www.nitc.ac.in)

### **About the Department:**

Established in 1961, the Electrical Engineering Department offers one 4 year B.Tech degree programme in Electrical & Electronics Engineering, and four regular M.Tech programmes, viz., Instrumentation & Control Systems, Power Systems, Power Electronics and Industrial Power & Automation. The research programmes leading to Ph.D. Degree in the broad areas of Electrical Sciences and Engineering and the sponsored research programmes funded by various agencies are also undertaken by the faculty. The Department is also actively engaged in testing and consultancy and is a recognized QIP centre for Ph.D and M.Tech.

### **About the Institute:**

National Institute of Technology Calicut (NITC), originally established in 1961 as Regional Engineering College Calicut, is fully centrally funded by MHRD and is governed by the NIT Act 2007. Institute has ten departments, three schools and nine research centers. It offers ten UG, and twenty five PG programmes along with the Ph.D programme in various fields of Science, Technology and Engineering. Faculties in the various Departments have active collaborations with universities and elite institutions within and outside India.

### **Calicut City:**

Calicut, also known as Kozhikode, is a blooming city in the region of Malabar. This region is a major knowledge hub of Kerala and proudly hosts many institutions of national eminence like NITC, IIMK, NIELIT, CWRDM, Kerala School of Mathematics, IISR etc. Calicut is well connected by direct trains/road/air to all major cities in India. Apart from the serene beaches on the west and the high ranges of the Western Ghats on the east, there are many landmark places that attract attention of the tourists. NITC is 22km off the city limits towards east.

## MHRD and TEQIP Sponsored Faculty Development Programme on



**22<sup>nd</sup> June – 28<sup>th</sup> June, 2014**

### **Coordinators**

**Dr. Sindhu T.K.**

**Dr. Preetha P.**

### **Organized By**



**Electrical Engineering Department  
National Institute of Technology Calicut  
NIT Campus P.O., Calicut – 673601**

## Preamble:

Demand for electricity is growing at an accelerated pace. To meet the growing demand, the generation capacity is to be increased as well as efficiently transported over large distances with minimum losses. It is observed that an UHV transmission line would be more suitable for transfer of bulk power efficiently. Thus it is essential that the electrical engineers need be equipped with the latest research and development issues in high voltage technology. This emphasizes the need for a platform for teachers of engineering/technical institutions and practicing high voltage engineers to learn modern techniques for increasing power generation as well efficiently transmitting it.

Size of the insulators required for transmitting large amount of power at high voltages can be optimized by the use of emerging nanodielectrics technology, which also requires the electric field analysis for insulator design. The design of relays and protection coordination also are very significant at high voltages. The proliferation of power electronic devices in the power system causes various electromagnetic interference issues which should be analyzed by simulations for proper design and operation of the system. This course aims to give an insight and research orientation to these topics.

## Major Topics:

- Developments in insulation technology
- Nanodielectrics and instrumentation
- Computer based Simulations for electric field analysis
- HVDC technology
- Electromagnetic interference and compatibility
- Development in protective relaying

## Resource persons:

All the sessions will be handled by Faculty of NITC and Invited experts from Academic/Research Institutions.

## Eligibility:

The course is open to faculty from AICTE approved Engineering colleges/Institutions and working professionals from Industries/R&D organizations with basic degree in relevant field of Electrical Engineering.

## Registration:

Teachers from AICTE approved institutions (Refundable)	Rs. 1,000/-
All others	Rs. 5,000/-

Duly filled up registration form as per the format given in the brochure along with the DD for registration fee and a self addressed envelope may be sent to the Coordinator so as to reach on or before **26<sup>th</sup> May 2014**. For faculty from AICTE approved institutions, if selected, the DD amount will be refunded on successful completion of the course and if not selected, the DD will be sent back by post. For working professionals from Industries/ R&D organizations the registration fee includes the charges of course materials, boarding and lodging. **Number of participants is restricted to thirty five. Early intimation on participation through e-mail is highly appreciated.** DD shall be drawn in favor of "Coordinator RTAHVE, NIT Calicut" payable at SBI NIT Calicut (Code: 2207).

## Accommodation:

Lodging and Boarding will be provided in the NITC campus for all selected participants if they are from the AICTE approved institutions.

## Travel Expenses:

Second sleeper class to- and- fro rail fare by the shortest route will be reimbursed to the participants sponsored from the AICTE approved institutions.

## Faculty Development Programme on *Recent Trends and Applications of High Voltage Engineering*

22<sup>nd</sup> June – 28<sup>th</sup> June, 2014.

## REGISTRATION FORM

1. Name: .....
2. Date of birth: ..... Gender(M/F): ...
3. Designation: .....
4. Department: .....
5. Institution: .....
6. Address for communication:  
.....  
.....  
Pin .....Phone No: .....
- Mobile: .....
- e-mail: .....
7. Highest Qualification: .....
8. Specialization: .....
9. Experience (Number of years):  
Teaching: ..... Industry: .....
10. Whether accommodation needed (Yes/No).....
11. DD No: and date: .....