About IEEE Power & Energy Society: The IEEE Power & Energy Society is a worldwide, non-profit association of more than 32,000 industry professionals, academics and students with a common interest in the electric power energy industry. The society aims for developing standards of equipment and systems, for educating members of the industry and the general public. The Society embraces research, development, planning, design, construction, maintenance, installation and operation of equipment, structures, materials and power systems for the safe, sustainable, economic and reliable conversion, generation, transmission, distribution, storage and usage of electric energy, including its measurement and control.

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NATIONAL WORKSHOP ON XILINX VIVADO BASED FPGA DESIGN AND ZYNQ ARCHITECTURE

September 3rd-4th ,2016

Registration Form

Please complete the details below and mail along

with the Registration fee.

1. Name Mr./Ms.……………………………

2. Category: Student/Faculty/Industry

3. Organization/Institute: ....

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4. M.Tech/B.Tech/Diploma:...........................

5. Specialization: ...………………………

6. Address:………………………………. .

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7. Mob No: ....

8. E-mail ID:...

9. Online Payment Ref

No: ....

10. Accommodation Required: Yes/No

11. If Yes: International Hostel/Students Hostel

Signature

NATIONAL WORKSHOP
on
XILINX VIVADO BASED FPGA DESIGN AND ZYNQ ARCHITECTURE

INDUSTRIAL POWER GROUP DEPARTMENT OF ELECTRICAL ENGINEERING NIT CALICUT

In Association with

IEEE POWER & ENERGY SOCIETY STUDENT CHAPTER NIT CALICUT

In Participation with

Preamble:

The Vivado Design suite supports 7-Series, Zynq, and UltraScale All Programmable families. It is developed to address the productivity bottlenecks in system-level design, integration, and implementation. The Vivado Design suite provides ease-of-use, system level integration capabilities, and new tools and methodologies, increasing overall productivity. Therefore, this training is designed for students and faculties of all branches and who are associated with Xilinx Programmable devices in digital design. It will offer a unique opportunity to the students and researchers working in the relevant areas of Embedded system and digital design to come closer through theoretical sessions and hands on training. Students, academicians and Industry professionals are welcome to participate in this national workshop.

Course Content:

- Introduction to Embedded System Design using Zynq
- Lab 1: Simple Hardware Design
- Zynq Architecture
- Extending the Embedded System into Programmable Logic
- Adding Your Own IP Peripheral
- Lab 2: Creating and Adding Your Own Custom IP
- Telecommunication IP cores access demo
- Introduction to Digilent and CoreEL
- Products offering from Digilent
- Embedded Linux Overview
- Lab 1: A First Look
- Compare and contrast embedded Linux on Xilinx ZynqSoC and Linux on desktop.
- Introduction to PetaLinux Tools
- Demo for Analog Discovery on Zeboard

Speakers:
Faculty experts from NITC
Professional Experts from CoreEl Technologies

Who Shall Benefit?
UG/PG students, Faculties of Engineering Institutes and working professionals.

Registration Fee covers course material and working refreshments only.
IEEE Student Members: Rs 500/-
Non-IEEE Student Members: Rs 750/-

IEEE Members: Rs 1000/-
Non-IEEE Members: Rs 1250/-

Online Payment:
Account No: 34385865770
IFSC code: SBIN0002207, SBI REC Chathamangalam.
(Enter your name so that receipt bears your name).

Registration Procedure:
Applicants are encouraged remit the course fee via NEFT online transfer and thereafter follow the link http://goo.gl/IELYFH to complete the registration process.
Limited seats, if vacant will be considered for spot registration.

Important Dates:
Last date for registration: **1st September 2016**
Selection intimation to the applicant: **2nd September 2016**

Boarding and Lodging:
Accommodation can be arranged in the International hostel/Guest House/Student Hostel subject to availability and on prior payment.
International Hostel: Rs. 450/- (Triple Sharing per day)
Student Hostel: Rs: 250/- (Twin sharing per day)
The scanned copy of the fee receipt, IEEE membership proof (if applicable) shall be sent as an E-mail attachment to elzsv2016@gmail.com

NIT Calicut: National Institute of Technology Calicut was founded as Regional Engineering College, Calicut in 1961. Set in a picturesque at the foothills of the Western Ghats, it is located about 22 kilometers north-east of Calicut city. It is prestigious institute with a reputation for excellence at both undergraduate, postgraduate and research levels, fostering the spirit of national integration among the students and a close interaction with industry.

Dept. of Electrical Engineering: Established in 1961, the Department of Electrical Engineering of the National Institute of Technology Calicut offers programmes leading to Bachelor's Degree, Master's Degree as well as Ph.D. The four year undergraduate programme leads to the Bachelor of Technology (B. Tech) degree in Electrical and Electronics Engineering. Specializations for the Master's level programmes are (i) Instrumentation & Control Systems (ii) Power Systems (iii) Power Electronics and (iv) Industrial Power and Automation, (v) High Voltage Engineering. The major research groups in the Electrical Engineering department are Control & Instrumentation, Power & Energy, Machines & Power Electronics, Industrial Power & Automation and High Voltage Engineering. In addition to these regular programmes, this department is also actively involved in conducting faculty development programmes, job-oriented short-term training programmes, continuing education programmes for engineering professionals and academic faculty.

Industrial Power Group is one of the leading research group in National Institute of Technology, Calicut. The main objective of the group is to develop new technologies in the field of Industrial Power and Automation and equip engineers with them. It is an interdisciplinary group with faculty, students, Research scholars and working professionals from different departments of NIT Calicut.