# INTRODUCTION TO MULTILEVEL INVERTERS

Rijil Ramchand Associate Professor NIT Calicut

# What is power electronics?

- Definition
- Conversion of electric power
- > The interdisciplinary nature
- Position and significance in the human society

# What is power electronics?

#### > Power Electronics:

- is the electronics applied to conversion and control of electric power.
- $\succ$  Range of power scale :
  - milliwatts(mW) megawatts(MW) gigawatts(GW)

- > A more exact explanation:
- $\succ$  The primary task of power electronics is to process and control the flow of electric energy by supplying voltages and currents in a form that is optimally suited for user loads.

#### Conversion of electric power



#### Classification of power converters

Power output Power input	DC	AC
AC	AC to DC converter (Rectifier)	AC to AC converter (Fixed frequency : AC controller Variable frequency: Cycloconverter or frequency converter)
DC	DC to DC converter (Chopper)	DC to AC converter (Inverter)

#### Power electronic system

Generic structure of a power electronic system



> Control is invariably required.

Power converter along with its controller including the corresponding measurement and interface circuits, is also called power electronic system.

#### Typical power sources and loads for a power electronic system



The task of power electronics has been recently extended to also ensuring the currents and power consumed by power converters and loads to meet the requirement of electric energy sources.

#### The interdisciplinary nature

#### William E. Newell's description



#### Relation with multiple disciplines



Power electronics is currently the most active discipline in electric power engineering worldwide.

Position and significance in the human society

- Electric power is used in almost every aspect and everywhere of modern human society.
- Electric power is the major form of energy source used in modern human society.
- The objective of power electronics is exactly about how to use electric power, and how to use it effectively and efficiently, and how to improve the quality and utilization of electric power.
- Power electronics and information electronics make two poles of modern technology and human society— information electronics is the brain, and power electronics is the muscle.



The thread of the power electronics history precisely follows and matches the break-through and evolution of power electronic devices

# Applications

- Industrial
- > Transportation
- > Utility systems
- Power supplies for all kinds of electronic equipment
- Residential and home appliances
- Space technology
- > Other applications



PEGCRES 2015

## Inverters - Introduction

Inverters convert DC voltage to variable magnitude, variable frequency AC voltage.

Ideally, purely sinusoidal output voltage.

- ➤ Practically not possible.
- PWM Techniques makes the task of extracting sinusoidal voltage from output of inverters easier.



## **Inverters - Introduction**

The DC source is usually composed of a rectifier followed by an energy storage or filter stage known as DC link – Indirect Conversion

➤CSI have been dominating in the medium-voltage high-power range with the pulse-width modulated CSI (PWM-CSI) and the load-commutated inverter (LCI)

➢Single-phase and three-phase two-level VSIs are widely used in low- and medium-power applications. Recently, VSI have also become attractive in the medium-voltage high-power market with multilevel inverter topologies

# Two-level Voltage Source Inverter



**Three-phase Two-level VSI feeding Induction Motor** 



PEGCRES 2015

Two-level Voltage Source Inverter  $\geq V_{AN}$ ,  $V_{BN}$  &  $V_{CN}$  are known as pole voltages  $\geq V_{An}$ ,  $V_{Bn}$  &  $V_{Cn}$  are known as phase voltages  $\succ V_{AB}$ ,  $V_{BC}$  &  $V_{CA}$  are known as line voltages  $V_{nN} = V_{nA} + V_{AN},$  $V_{nN} = V_{nR} + V_{RN} \&$  $V_{nN} = V_{nC} + V_{CN}$  $\therefore V_{nN} = \frac{(V_{AN} + V_{BN} + V_{CN}) + (V_{nA} + V_{nB} + V_{nC})}{3}$  $:: \left( V_{nA} + V_{nB} + V_{AN} + V_{BN} + V_{CN} \right)$ 

### Two-level Voltage Source Inverter



# Two-level Voltage Source Inverter



Voltage space vector structure generated by a two-level VSI

Multilevel Inverters - Introduction



PEGCRES 2015

# Multilevel Inverters - Introduction

#### Drawbacks of two-level VSIs for MV Drives

- ➢ High dv/dt in the inverter output voltage as high as 10,000V/µs
- Motor harmonic losses
- This can be solved by adding properly tuned LC filter.

It has some disadvantages

- Increased manufacturing cost
- Fundamental voltage drop
- Circulating current between the filter and DC circuit

# Multilevel Inverters - Introduction



Multilevel inverter output voltage: (a) two-level and (b) nine-level.

# Evolution of Multilevel Space vector structures

Hexagonal space vectors.



# Multilevel Voltage Source Inverter



PEGCRES 2015

## Multilevel Voltage Source Inverter

Multi-level inverters are the preferred choice in industry for the application in High voltage and High power application

Advantages of Multi-level inverters

- Higher voltage can be generated using the devices of lower rating.
- Increased number of voltage levels produce better voltage waveforms and reduced THD.
- Switching frequency can be reduced for the PWM operation.





Three-phase three-level diode-clamped converter also called NPC converter

28

- On the dc side of the inverter, the dc bus capacitor is split into two, providing a neutral point Z.
- > The diodes connected to the neutral point,  $D_{Z1}$  and  $D_{Z2}$ , are the clamping diodes.
- When switches  $S_2$  and  $S_3$  are turned on, the inverter output terminal A is connected to the neutral point through one of the clamping diodes.
- The voltage across each of the dc capacitors is E, which is normally equal to half of the total dc voltage  $V_d$ . With a finite value for  $C_{d1}$  and  $C_{d2}$ , the capacitors can be charged or discharged by neutral current  $i_Z$ , causing neutral-point voltage deviation.



Switching State	Terminal Voltage				
	S <sub>1</sub>	S <sub>2</sub>	S <sub>3</sub>	$S_4$	V <sub>AZ</sub>
Р	ON	ON	OFF	OFF	V <sub>d</sub> /2
Ο	OFF	ON	ON	OFF	0
Ν	OFF	OFF	ON	ON	-V <sub>d</sub> /2



Thereate

- ➢No dynamic voltage sharing problem: Each of the switches in the NPC inverter withstands only half of the total dc voltage during commutation.
- Static voltage equalization without using additional components: The static voltage equalization can be achieved when the leakage current of the top and bottom switches in an inverter leg is selected to be lower than that of the inner switches.
- ➤Low THD and dv/dt: The waveform of the line-to-line voltages is composed of five voltage levels, which leads to lower THD and dv/dt in comparison to the two-level inverter operating at the same voltage rating and device switching frequency.

### Diode Clamped (NPC) 4-level and 5level Inverters





PEGCRES 2015

### Diode Clamped (NPC) 4-level and 5level Inverters

SWITCH STATUS								
FOUR-LEVEL INVERTER							V <sub>AN</sub>	
S <sub>1</sub>	$\mathbf{S}_1$ $\mathbf{S}_2$ $\mathbf{S}_3$ $\mathbf{S}_1'$ $\mathbf{S}_2'$ $\mathbf{S}_3'$							
-1	1	1		0	0	0		<b>3</b> E
0	1	1		1	0	0		<b>2</b> E
0	0	1		1	1	0		Ε
0	0	0		1	1	1		0
FIVE-LEVEL INVERTER								
		FIVE	-LEVEI	L INVE	RTER			V
<b>S</b> <sub>1</sub>	S <sub>2</sub>	FIVE S <sub>3</sub>	-LEVEI <mark>S</mark> 4	L INVE	RTER S2'	S <sub>3</sub> ′	<b>S</b> <sub>4</sub> '	V <sub>AN</sub>
<b>S</b> <sub>1</sub> _1	<b>S</b> <sub>2</sub> 1	FIVE S <sub>3</sub> 1	-LEVEI <mark>S4</mark> 1	L INVE <b>S<sub>1</sub>'</b> 0	RTER S2' 0	<b>S<sub>3</sub>'</b>	<b>S<sub>4</sub>'</b> 0	V <sub>AN</sub> 4E
<b>S</b> <sub>1</sub> 1 0	<b>S</b> <sub>2</sub> 1 1	FIVE <b>S</b> <sub>3</sub> 1 1	-LEVEI <b>S</b> <sub>4</sub> 1 1	L INVE <b>S<sub>1</sub>'</b> 0 1	RTER <b>S<sub>2</sub>'</b> 0 0	<b>S<sub>3</sub>'</b> 0 0	<b>S</b> <sub>4</sub> ' 0 0	V <sub>AN</sub> 4E 3E
<b>S</b> <sub>1</sub> 1 0 0	<b>S</b> <sub>2</sub> 1 1 0	FIVE <b>S</b> <sub>3</sub> 1 1 1 1	-LEVEI <b>S</b> <sub>4</sub> 1 1 1	L INVE <b>S<sub>1</sub>'</b> 0 1 1	RTER <b>S<sub>2</sub>'</b> 0 0 1	<b>S<sub>3</sub>'</b> 0 0 0	<b>S</b> <sub>4</sub> ' 0 0	V <sub>AN</sub> 4E 3E 2E
<b>S</b> <sub>1</sub> 1 0 0 0	<b>S</b> <sub>2</sub> 1 1 0 0	FIVE <b>S</b> <sub>3</sub> 1 1 1 0	-LEVEI <b>S</b> <sub>4</sub> 1 1 1 1 1	L INVE <b>S<sub>1</sub>'</b> 0 1 1 1	RTER <b>S<sub>2</sub>'</b> 0 0 1 1	<b>S<sub>3</sub>'</b> 0 0 0 1	<b>S</b> <sub>4</sub> ' 0 0 0 0	V <sub>AN</sub> 4E 3E 2E E



### Diode Clamped (NPC) 4-level and 5level Inverters



## Diode Clamped (NPC) multilevel Inverters

#### **Component Count of Diode-Clamped Multilevel Inverters**

Voltage Level m	Active Switches 6(m-1)	Clamping Diodes <sup>a</sup> 3(m-1)(m-2)	DC Capacitors (m-1)
3	12	6	2
4	18	18	3
5	24	36	4
6	30	60	5
7	36	90	6

<sup>a</sup>All diodes and active switches have the same voltage rating.



## Diode Clamped (NPC) multilevel Inverters

Disadvantages

- Uneven loss distribution in the devices
  In a fundamental cycle, the conduction period of the
  - inner devices is more than the outer devices. This causes unequal losses in devices in a leg.
- The fluctuation of the dc bus midpoint voltage
- Additional clamping diodes.
- Complicated PWM switching pattern design

## Flying Capacitor 3-level Inverter



## Flying Capacitor 3-level Inverter



# Flying Capacitor 5-level Inverter



# Flying Capacitor 5-level Inverter

	Pole voltage,			
$S_1$	S <sub>2</sub>	S <sub>3</sub>	$S_4$	V <sub>AN</sub>
1	1	1	1	4E
1	1	1	0	
0	1	1	1	2E
1	0	1	1	5E
1	1	0	1	
1	1	0	0	
0	0	1	1	
1	0	0	1	25
0	1	1	0	ZE
1	0	1	0	
0	1	0	1	
1	0	0	0	Z
0	1	0	0	F
0	0	1	0	
0	0	0	1	
0	0	0	0	0

## Flying Capacitor Multilevel Inverters

#### **Component Count of Flying Capacitor Multilevel Inverters**

Voltage Level m	Active Switches 6(m-1)	Clamping Diodes	$\frac{\text{DC Capacitors}}{(m-1)+3^*(\sum_{k=1}^{m-2}k)}$
3	12	0	5
4	18	0	12
5	24	0	22
6	30	0	35
7	36	0	51



42

# Multilevel (3-level) Cascaded H-Bridge Inverters - with equal voltages



## Multilevel (3-level) Cascaded H-Bridge Inverters - with equal voltages

	Pole voltage,			
S <sub>1A</sub>	S <sub>2A</sub>	S <sub>3A</sub>	$\mathbf{S}_{4\mathrm{A}}$	V <sub>AN</sub>
1	0	0	1	E
1	0	1	0	
0	1	0	1	U
0	1	1	0	-E

#### Multilevel (5-level) Cascaded H-Bridge Inverters - with equal voltages



#### Multilevel (5-level) Cascaded H-Bridge Inverters - with equal voltages

Switching State			V	V	Pole voltage,	
<b>S</b> <sub>11</sub>	<b>S</b> <sub>31</sub>	S <sub>12</sub>	<b>S</b> <sub>32</sub>	$\mathbf{v}_{\mathrm{H1}}$	• <sub>H2</sub>	V <sub>AN</sub>
1	0	1	0	Ε	Ε	<b>2</b> E
1	0	1	1	Ε	0	
1	0	0	0	Ε	0	E E
1	1	1	0	0	Ε	
0	0	1	0	0	Ε	
0	0	0	0	0	0	
0	0	1	1	0	0	
1	1	1	1	0	0	0
1	1	0	0	0	0	0
1	0	0	1	E	-E	
0	1	1	0	<b>-</b> E	E	
0	1	1	1	<b>-</b> E	0	3
0	1	0	0	<b>-</b> E	0	F
1	1	0	1	0	<b>-</b> E	-E
0	0	0	1	0	<b>-</b> E	
0	1	0	1	-E	-E	-2E

Multilevel Cascaded H-Bridge Inverters – with equal voltages The number of voltage levels in a CHB inverter can be found from

m = (2H + 1)

where *H* is the number of H-bridge cells per phase leg.

The voltage level *m* is always an odd number for the CHB inverter while in other multilevel topologies such as diode-clamped inverters, it can be either an even or odd number.

The total number of **active switches (IGBTs)** used in the CHB inverters can be calculated by

 $N_{sw}=6(m-1)$ 

#### Multilevel Cascaded H-Bridge Inverters (7 and

9-level) – per phase diagram





PEGCRES 2015

#### Multilevel Cascaded H-Bridge Inverters with unequal voltages



#### Multilevel Cascaded H-Bridge Inverters with unequal voltages

Voltage Level and Switching State of the Two-Cell Seven-Level CHB Inverter with Unequal dc Voltages

Output Voltage	Switching State					
v <sub>AN</sub>	<i>S</i> <sub>11</sub>	S <sub>31</sub>	S <sub>12</sub>	S <sub>32</sub>	$v_{H1}$	V <sub>H2</sub>
3 <i>E</i>	1	0	1	0	E	2E
2E	1	1	1	0	0	2E
	0	0	1	0	0	2E
E	1	0	1	1	E	0
	1	0	0	0	E	0
	0	1	1	0	-E	2E
0	0	0	0	0	0	0
	0	0	1	1	0	0
	1	1	0	0	0	0
	1	1	1	1	0	0
-E	1	0	0	1	E	-2E
	0	1	1	1	-E	0
	0	1	0	0	-E	0
-2E	1	1	0	1	0	-2E
	0	0	0	1	0	-2E
-3E	0	1	0	1	-E	-2E

## Cascaded H-Bridge Multilevel Inverters

#### **Component Count of Cascaded H-Bridge Multilevel Inverters**

Voltage Level m	Active Switches 6(m-1)	Clamping Diodes	DC Sources
3	12	0	3
5	24	0	6
7	36	0	9
9	48	0	12

# References

- B. Wu, High-Power Converters and AC Drives, Wiley-IEEE Press, Piscataway, NJ, 2006.
- J. Rodriguez, J. S. Lai, and F. Z. Peng, Multilevel inverters: A survey of topologies, controls, and applications, IEEE Transactions on Industrial Electronics, 49(4), 724–738, August 2002.
- N. Mohan, T. M. Undeland, and W. P. Robbins, Power Electronics: Converters, Applications, and Design, 3 edn, Wiley, Hoboken, NJ, October 10, 2002.
- Rodriguez, S. Bernet, B. Wu, J. O. Pontt, and S. Kouro, Multilevel voltage-source-converter topologies for industrial medium-voltage drives, IEEE Transactions on Industrial Electronics, 54(6), 2930–2945, December 2007.

