

# Electrical Engineering Association

## Recent Events (2022-23)

Si. No.	Event Name	Event Date
1.	Research Talks 2	03-11-2022
2.	Research Talks 1	27-10-2022
3.	EEA Onam	05-09-2022
4.	Painting Competition Associated with 75th year of Indian Independence	12-08-2022

## Painting Competition Associated with 75<sup>th</sup> year of Indian Independence

Electrical Engineering Association, NIT Calicut organized a painting competition on 12<sup>th</sup> August to commemorate 75<sup>th</sup> year of independence. Theme was 75 years of Indian Independence.



### *Notice for the event*

The venue was EED Seminar Hall. Prof Sindhu TK (HoD in charge) inaugurated the event at 3 PM. Dr Ritesh Ranjan from the Department of Architecture agreed to judge the paintings of students and select the best painting.



*Student taking part in the painting competition*



*Winner of the painting competition receiving her prize.*



*First and second prize winning paintings.*

The award for the best painting was done by Prof Sindhu. Dr Mithun MS and Dr Rakesh R Warier coordinated the event.

## EEA Onam'22

The Electrical Engineering Association conducted its department Onam at the Bhaskara Hall from 5 PM to 10 PM on September 5<sup>th</sup> 2022. A meet-up of all EEE students and faculties, it was a blend of fun and thrill. The event had a great footfall of around 500 students. A grand Pookkalam was also put before the Bhaskara Hall.

The event began with the official ceremony with Dr. Sindhu lighting the lamp. It was followed by a welcome address by Dr. Rakesh Warriar and a closing address by Dr. Mithun M S.

After the official ceremony, the payasam was distributed to the faculties and students. Meanwhile, students were organized for the Onam games. The following three games were conducted in multiple rounds, which had maximum participation from all years:

- 1) Musical Chairs
- 2) Lemon-spoon race
- 3) Tug of War/Vadam vali

Following the conclusion of the games, we navigated into the Cultural Programmes. Enthusiastic students from the 2nd, 3rd, and 4th years showcased their talents in various events. There were around 12 events which included solo songs, group songs, solo dances, and group dances. The program started with a piece of eloquent music by Nasrin and ended with thrilling dance performances by B21s, B20s, and B19s. The night also saw a mind-blowing musical performance by the B20s, bewitching solo dances and enthralling songs. The event concluded by 10 PM.





Cultural Programmes:



# RESEARCH TALKS

## The inaugural session on 27th Oct 2022, 4:30 PM

'*Research Talks*' is a platform where research scholars, faculty members, and master's students can share, upgrade and update their knowledge. The talks aim to break individual research's subtle boundaries and encourage peer research.

Any research students can use this platform to enhance their knowledge, troubleshoot technical issues faced during research, discuss topics of common interest, make aware of vast areas in electrical engineering, etc.

The inaugural session of the talk started with a motivational lecture from Professor **Dr. Rijil Ramchand** on 27/10/2022 from 4:30 PM to 5:30 PM at **DB206** (near to EED library).

The meetings are planned for every Thursday. The talk is open to ALL research scholars, irrespective of their area of research in electrical engineering. The discussion topics can be any technical topic of common interest.



The poster is divided into two main sections. The left section has a teal background and features the text 'Inaugural Talk' in a white script font at the top. Below it is a circular portrait of Dr. Rijil Ramchand, a man with glasses and a mustache, wearing a striped shirt. Underneath the portrait, his name 'Dr. Rijil Ramchand' is written in white, followed by 'Professor, EED, NITC' in a smaller font. The right section has a light grey background. At the top right is the NITC logo. Below it is a microphone icon followed by the text 'RESEARCH TALKS' in a bold, sans-serif font. A horizontal yellow line separates this from the event details: 'Venue: PG Seminar Hall, EED', 'Date: Oct 27<sup>th</sup> 2022, Thursday', and 'Time: 4:30 pm'. Another horizontal yellow line follows. Below this is the text 'in association with EEA, NITC' and the EEA logo. At the bottom, it says 'Electrical Engineering Department, NITC'.

Many faculty members, research scholars, and master's degree students were present for the new beginning of the interactive session. Dr. Rijil Ramchand gave an exciting lecture on general engineering topics and how the research students must conduct the research.





**The second session 3rd Nov 2022, at 4:30 PM.**

The second session of the talk was given by Mr.Kannan SA, a research scholar of the electrical engineering department, on 3rd Nov 2022, 4:30 PM at PG Seminar hall, EED. The topic of the talk was '**PCB Design challenges in Solar Converters.**'

He elaborated on the common challenges faced in PCB design and pointed out mistakes one would make during the initial stages of PCB design. He also demonstrated some PCB designs using the software DipTrace. DipTrace is a schematic and PCB design software that can also be used for 3D modeling.



The poster is divided into three horizontal sections. The top section is light grey and features a microphone icon on the left and the text 'RESEARCH TALKS' in large, bold, black letters. A small circular logo is in the top right corner. The middle section is yellow and contains the event details: 'Venue: PG Seminar Hall,EED', 'Date: Nov 3<sup>rd</sup> 2022,Thursday', and 'Time: 4.30 - 5.30 pm'. Below this, it lists the 'Speaker : Kannan S A, Research Scholar EED' and the 'Topic : PCB Design challenges in Solar Converter' in red text. At the bottom of this section, it says 'In association with EEA,NITC' next to the logo of the Electrical Engineering Association. The bottom section is teal and features a stylized circuit board pattern with various components and traces.

**RESEARCH TALKS**

Venue: PG Seminar Hall,EED  
Date: Nov 3<sup>rd</sup> 2022,Thursday  
Time: 4.30 - 5.30 pm

Speaker : Kannan S A, Research Scholar EED

Topic : **PCB Design challenges in Solar Converter**

In association with EEA,NITC



Most attendees found the talk very helpful for their research work as it demonstrated the practical side of PCB design.